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**(54) Impulse noise detector and noise reduction system**

Störimpulsdetektor und Störimpulsreduziersystem

Détecteur de bruit impulsionnel et système réducteur de bruit

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**EP 0 930 719 B1**

## Description

### BACKGROUND OF THE INVENTION

**[0001]** The present invention relates to an impulse noise detector useful in, for example, a car radio.

**[0002]** Impulse noise such as ignition noise and mirror noise occurs frequently in the car environment, and is readily picked up by the antenna of a car radio. To prevent such impulse noise from becoming audible, a car radio requires an impulse noise reducer.

**[0003]** FIG. 1 is a block diagram of a frequency-modulation (FM) stereo car radio comprising an antenna 1, a tuner 2, an FM demodulator (DEM0D) 3, an impulse noise reducer 4, an impulse noise detector 5, an FM stereo demodulator 6, and a pair of loudspeakers 7 (only one of which is visible). An FM radio-frequency broadcast signal received by the antenna 1 is selected and amplified by the tuner 2, then demodulated by the FM demodulator 3 to obtain an FM composite signal. The FM composite signal includes a left-right sum component with audio frequencies up to fifteen kilohertz (15 kHz), a 19-kHz pilot component, and a left-right difference component amplitude-modulated around a 38-kHz carrier frequency, with an upper sideband extending to 53 kHz. Impulse noise picked up by the antenna 1 may also be present, but this noise is detected by the impulse noise detector 5 and blanked out by the impulse noise reducer 4. Further demodulation of the FM composite signal by the FM stereo demodulator 6 produces right-channel and left-channel audio signals, which are reproduced through the loudspeakers 7.

**[0004]** FIG. 2 shows the internal structure of an impulse noise reducer 4 and an impulse noise detector 5 employed in the prior art. The FM composite signal is received at an analog input terminal 8. The impulse noise detector 5 comprises a high-pass filter (HPF) 9, an amplifier (AMP) 10, an automatic-gain-control (AGC) circuit 11, and a monostable multivibrator 12. The impulse noise reducer 4 comprises a gate 13. The high-pass filter 9 has a cutoff frequency high enough to reject all of the above-mentioned components of the FM composite signal. Impulse noise includes still higher frequency components that are not rejected. The output of the high-pass filter 9, which comprises impulse noise and other noise, is amplified by the amplifier 10, the gain of which is controlled by the AGC circuit 11 so as to keep the amplified noise signal below the threshold of the monostable multivibrator 12. The AGC circuit 11 is designed to respond slowly to changes in the noise level, however. When impulse noise occurs, the monostable multivibrator 12 cannot track the rapid rise in the noise level, so the output of the amplifier 10 triggers the monostable multivibrator 12, which outputs an impulse noise detection signal to the gate 13. The output of the gate 13 is held constant while the impulse noise detection signal is active, so impulse noise is effectively suppressed.

**[0005]** One problem in this prior-art circuit is that the monostable multivibrator 12 is triggered not only by impulse noise, which typically lasts only a few tens or a few hundreds of microseconds, but also by tone-burst signals, in which a sine-wave signal lasting several milliseconds is preceded and followed by a signal with zero amplitude. The result is the unwanted blanking of these tone bursts.

**[0006]** Another problem is that intermodulation distortion can cause some of the energy of the composite FM signal to leak into frequency bands above 53 kHz, affecting the operation of the AGC circuit 11 and making accurate detection of impulse noise difficult.

**[0007]** An example of a document reflecting the above prior art is Japanese Patent Kokai Publication No. 63-182916.

**[0008]** EP-A-0 377 965 discloses a radio circuit having a noise detector for detecting impulse noise by developing an average noise signal and comparing the average noise signal with an attenuated value of the instantaneous noise signal to generate a noise flag.

### SUMMARY OF THE INVENTION

**[0009]** It is accordingly an object of the present invention to detect impulse noise lasting for brief intervals of time, while avoiding the detection of signals that appear abruptly but last for longer intervals.

**[0010]** Another object of the invention is to detect impulse noise accurately in the presence of leakage of audio signal components caused, for example, by intermodulation.

**[0011]** Aspects of the invention are set out in the accompanying claims.

**[0012]** By using a multiple of the envelope signal averaged over a time interval as a detection threshold, the invented method detects short-duration impulse noise without detecting abruptly rising signals of longer duration.

**[0013]** By taking an envelope of differences of a preliminary envelope, the invented method emphasizes impulse noise, relative to intermodulation products and other audio leakage, so that the impulse noise can be accurately detected.

### BRIEF DESCRIPTION OF THE DRAWINGS

**[0014]** In the attached drawings:

FIG. 1 is a block diagram of an FM stereo radio receiver;

FIG. 2 is a more detailed block diagram showing the internal structure of a prior-art impulse noise detector;

FIG. 3 is a block diagram of an impulse noise detector illustrating a first example;

FIG. 4 is a graph illustrating the detection of impulse noise in the first example;

FIG. 5 is a graph illustrating the non-detection of a tone burst in the first example;

FIG. 6 is a block diagram of an impulse noise detector illustrating a first embodiment of the invention;

FIG. 7 is a graph illustrating the detection of impulse noise in the first embodiment;

FIG. 8 is a graph illustrating the non-detection of a tone burst in the first embodiment;

FIG. 9 is a block diagram of an impulse noise detector illustrating a second embodiment of the invention;

FIG. 10 is a graph illustrating impulse noise mixed with intermodulation products;

FIG. 11 is a graph illustrating the emphasis of the impulse noise by the difference processor in the second embodiment;

FIG. 12 is a block diagram of an impulse noise detector illustrating a third embodiment of the invention;

FIG. 13 is a block diagram of an impulse noise detector illustrating a fourth embodiment of the invention; and

FIG. 14 is a block diagram of an FM stereo radio receiver with a noise reduction system illustrating a second example.

#### DETAILED DESCRIPTION OF THE INVENTION

[0015] Embodiments of the invention will be described with reference to the attached drawings, in which like parts are indicated by like reference characters.

[0016] Referring to FIG. 3, a first example has an analog input terminal 8 receiving an analog audio signal, in this case an FM composite signal, and an output terminal 14 from which an impulse noise detection signal is output. The first example comprises an analog-to-digital converter (ADC) 15, a high-pass filter 9a, an envelope detector 16, a memory 17, an averager (AVG) 20, a multiplier 21, and a comparator (COMP) 22. The analog-to-digital converter 15 is a well-known hardware device. The other elements may be separate hardware devices, or software modules in a program executed by a computing device such as a microprocessor or digital signal processor (DSP).

[0017] Next, the operation of the first example will be described.

[0018] The analog-to-digital converter 15 converts the FM composite signal received at the analog input terminal 8 to a digital signal for input to the high-pass filter 9a. The high-pass filter 9a attenuates frequency components up to at least 53 kHz, and passes the remaining high-frequency components to the envelope detector 16. Since the high-pass filter 9a attenuates all of the signal frequencies normally present in the FM composite signal, the output of the high-pass filter 9a is essentially a noise signal, the level of which is substantially unaffected by variations in the left-channel and right-channel audio signal levels.

ected by variations in the left-channel and right-channel audio signal levels.

[0019] The envelope detector 16 determines the amplitude level of the output of the high-pass filter 9a. Various methods of envelope detection can be employed. In one exemplary method, the envelope detector 16 squares the input data received from the high-pass filter 9a, performs a low-pass filtering operation on the squared values, and takes the square roots of the low-pass filtered values. In another exemplary method, the envelope detector 16 performs a low-pass filtering operation on the absolute values of the data received from the high-pass filter 9a. The data output from the envelope detector 16 are stored temporarily in the memory 17.

[0020] The signal input at the analog input terminal 8 always includes a certain amount of low-level background noise, and intermittently includes impulse noise. The background noise is generated by electronic components in the preceding circuits in the receiver, such as the tuner 2 and FM demodulator 3 shown in FIG. 1. Because of automatic gain control in the preceding circuits, the level of the background noise and impulse noise depends on the strength of the FM signal received at the antenna, but impulse noise that does not greatly exceed the background noise level is masked by the background noise and does not have to be blanked out. The first example accordingly detects impulse noise by comparison with a threshold set in relation to the background noise level.

[0021] This comparison is made by the comparator 22, which compares each data value D stored in the memory 17 with a threshold set by the averager 20 and multiplier 21, drives the impulse noise detection signal to the active logic level if D exceeds the threshold, and drives the impulse noise detection signal to the inactive level if D does not exceed the threshold.

[0022] The averager 20 takes the average of the data values spanning a time interval preceding and following the data value D compared by the comparator 22, the interval being longer than the expected duration of impulse noise. An example of an appropriate interval is a two-millisecond interval extending one millisecond before and after the processing point of data D.

[0023] The multiplier 21 multiplies the average calculated by the averager 20 by a factor M, and supplies the result to the comparator 22 as the threshold value. The factor M should be high enough to avoid the unwanted detection of background noise, and of tone bursts and other abruptly rising signals that are more persistent than impulse noise. An example of an appropriate factor is four ( $M = 4$ ).

[0024] FIG. 4 illustrates the detection of a noise impulse with a duration of 0.2 milliseconds, when the averaging interval is two milliseconds and M is equal to four. Time is indicated on the horizontal axis. Data values are indicated on the vertical axis, normalized so that the noise impulse has a level of unity. For simplicity, the

envelope data EN stored in the memory 17 are shown as having a square waveform. The background noise level LN is close to zero. When the comparator 22 compares the data value at the leading edge of the noise impulse (D), the average data value AV in the surrounding two-millisecond interval is substantially equal to 0.1, and the threshold value TH set by the multiplier 21 is substantially  $0.1 \times 4$ , or 0.4. The data value D exceeds the threshold TH ( $1 > 0.4$ ), so the impulse noise detection signal is activated.

**[0025]** As further data arrive from the envelope detector 16, the compared data value (D) and the surrounding two-millisecond interval are shifted forward on the time axis, and the same process is repeated. The process can be repeated at each new data value, or at intervals of two or more new data values. The impulse noise detection signal will remain active for substantially the 0.2-millisecond duration of the noise impulse, then become inactive as the processing point moves past the noise impulse and the data value falls below the threshold level again.

**[0026]** FIG. 5 illustrates the response of the first example to a tone burst lasting longer than one millisecond, using the same notation as in FIG. 4. The tone burst is shown as rising abruptly to a level of unity at the current processing point (data D). The average value (AV) calculated around this point is substantially 0.5, so the threshold level (TH) is substantially equal to two ( $0.5 \times 4$ ). The data value D is less than the threshold TH ( $1 < 2$ ), so the impulse noise detection signal remains inactive. As the processing point shifts forward (to the right), the impulse noise detection signal will continue to remain inactive.

**[0027]** The first example is accordingly able to detect short-duration impulse noise without mistakenly detecting tone bursts and other signals of longer duration that should not be removed from the FM composite signal, even if these signals rise abruptly.

**[0028]** In a variation of the first example, the multiplier 21 multiplies the average value output from the averager 20 by one factor ( $M_1$ ) if the impulse noise detection signal is currently inactive, and by another, smaller factor ( $M_2$ ) if the impulse noise detection signal is currently active. For a given average value, the activation threshold of the impulse noise detection signal becomes higher than the deactivation threshold (because  $M_1 > M_2$ ). This variation provides a desirable hysteresis effect that keeps the impulse noise detection signal from fluctuating between the active and inactive states when an impulse noise waveform fluctuates around the detection threshold while rising or falling.

**[0029]** In another variation of the first example, the averaging interval is not shifted each time the compared data point D is shifted; the same average value is used during two or more comparisons. If the length of the averaging interval is two milliseconds, a new average value can be calculated once every millisecond, for example, or once every two milliseconds.

**[0030]** In another variation, the memory 17 is omitted and the averager calculates average values by, for example, a moving-average method.

**[0031]** In a further variation, the analog-to-digital converter 15 is omitted, and the high-pass filter 9a and envelope detector 16 are analog circuits. The averager 20, multiplier 21, and comparator 22 may also be analog circuits; alternatively, an analog-to-digital converter may be provided on the output side of the envelope detector 16.

**[0032]** These variations of the first example also apply to the embodiments described below.

**[0033]** Next, a first embodiment will be described.

**[0034]** Referring to FIG. 6, the first embodiment comprises the elements described in the first example. Except for the averager 20, these elements operate as in the first example. The first embodiment also comprises an additional averager 18 and an additional multiplier 19. Averager 18 takes the average of the data values stored in the memory 17 over a predetermined interval. Multiplier 19 multiplies the resulting average by a constant factor N. Averager 20 re-calculates the average value of the data in the memory 17 over the same interval, using only data values equal to or less than the product output by multiplier 19. Multiplier 21 multiplies the average value output by averager 20 by a factor M to obtain the threshold supplied to the comparator 22.

**[0035]** FIG. 7 illustrates the operation of the first embodiment in detecting a 0.2-millisecond noise pulse in envelope data EN with a low background noise level LN, the horizontal axis representing time and the vertical axis representing signal level. The height of the noise pulse is again normalized to unity. The factors M and N are both equal to four. The average value AV calculated by averager 18 over the indicated two-millisecond interval on the horizontal axis is 0.1. The product PR output by multiplier 19 is equal to 0.4. Averager 20 selects and averages the data values equal to or less than 0.4 in the same two-millisecond interval. All such data values are equal to LN, making the average value calculated by averager 20 also equal to LN. The threshold TH calculated by multiplier 21 is equal to four times LN. The first data point D in the noise impulse exceeds this threshold TH, causing output of an active impulse noise detection signal as in the first embodiment.

**[0036]** FIG. 8 illustrates the response of the first embodiment to a tone burst lasting longer than one millisecond, using the same notation as in FIG. 7, the level of the tone burst being normalized to unity. Averager 18 calculates an average value AV of 0.5, which multiplier 19 multiplies by four to obtain a product PR equal to two. Averager 20 selects the data values less than PR, thus selecting all data values in the interval, and again calculates an average value of 0.5. Multiplier 21 multiplies this average by four, obtaining a threshold value TH again equal to two. The data value D is less than this threshold TH, so the impulse noise detection signal remains inactive.

**[0037]** Like the first example, the first embodiment detects short-duration impulse noise without detecting abruptly rising signals of longer duration, such as tone bursts. For the idealized waveforms shown in FIGs. 7 and 8, the first embodiment produces the same results as the first example, but in the general case, the first embodiment detects impulse noise more quickly and more sensitively than the first example, because the threshold TH in the first embodiment is substantially unaffected by the noise impulses themselves.

**[0038]** Next, a second embodiment will be described.

**[0039]** Referring to FIG. 9, the second embodiment inserts a difference processor 23 between the envelope detector and memory of the first embodiment, so that the memory 17b stores the output of the envelope detector 16 as processed by the difference processor 23, instead of directly storing the output of the envelope detector 16. The difference processor 23 takes the difference between each data value received from the envelope detector 16 and the n-th preceding data value, where n is a fixed positive integer, and performs another envelope detection process on the resulting differences.

**[0040]** The other elements of the second embodiment are identical to the corresponding elements in the first embodiment, and operate in the same way.

**[0041]** The effect of the difference processor 23 is to emphasize rapidly-changing signal components, such as impulse noise, relative to more slowly-changing components, such as components of the FM composite signal that leak into the high-frequency band through intermodulation distortion. FIG. 10 shows an example of an envelope waveform output by the envelope detector 16, including both impulse noise 24 and intermodulation products 25. FIG. 11 shows the same waveform after passage through the difference processor 23, which took the difference between each data point and the data value four points before ( $n = 4$ ). The rapid changes in the impulse noise 24 leave a strong peak in the envelope signal output by the difference processor 23, while the intermodulation products 25 are much attenuated.

**[0042]** By attenuating high-frequency intermodulation products and thereby emphasizing impulse noise, the second embodiment enables impulse noise to be detected accurately despite the leakage of intermodulation products from the audio components of the FM composite signal into the high-frequency noise band.

**[0043]** Next, a third embodiment will be described.

**[0044]** Referring to FIG. 12, the third embodiment supplies the output of the analog-to-digital converter 15 to a pair of bandpass filters (BPFs) 9c and 9d having different passbands. As one example, the first bandpass filter 9c has a passband centered at 90 kHz while the second bandpass filter 9d has a passband centered at 100 kHz, both passbands having a bandwidth of 10 kHz. The output of the first bandpass filter 9c is processed by an envelope detector 16c, a memory 17c, an averager 18c, a multiplier 19c, another averager 20c, another multiplier 21c, and a comparator 22c, which are identical

to the corresponding elements in the first embodiment. The output of the second bandpass filter 9d is likewise processed by an envelope detector 16d, a memory 17d, an averager 18d, a multiplier 19d, another averager 20d, another multiplier 21d, and a comparator 22d identical to the corresponding elements in the first embodiment. The two comparators 22c and 22d output respective detection signals as described above.

**[0045]** The outputs of the two comparators 22c and 22d are supplied to an output logic processor 26, which supplies an output impulse noise detection signal to the output terminal 14. The output logic processor 26 also receives an indicator signal from a digital input terminal 27, indicating the level of the radio-frequency FM broadcast signal received at the antenna. In the third embodiment, the indicator signal is preferably a one-bit signal that is active when the received level of the broadcast signal exceeds a predetermined level.

**[0046]** When the indicator signal is active, the output logic processor 26 performs OR logic on the inputs from the two comparators 22c, 22d, activating the impulse noise detection signal when the output of either one (or both) of the comparators is active. When the indicator signal is inactive, the output logic processor 26 performs AND logic, activating the impulse noise detection signal only when the inputs from both comparators 22c, 22d are active. The output logic processor 26 accordingly activates the output impulse noise detection signal when at least two signals among the indicator signal, the output of the first comparator 22c, and the output of the second comparator 22d are active.

**[0047]** When the received radio-frequency signal level is high, the amplifiers in the front end of the receiver (not visible) are set to a comparatively low gain, reducing the amplitude of impulse noise picked up by the antenna in relation to the level of background noise generated by subsequent receiver circuits, thereby making the impulse noise harder to detect. The frequency spectrum of impulse noise fluctuates rapidly, however, while the spectrum of the background noise changes little over time. As a result, while the impulse noise may not be detected in the outputs of both bandpass filters 9c and 9d simultaneously, it is generally detectable in the output of one bandpass filter or the other.

**[0048]** When the received radio-frequency signal level is low, the front-end amplifiers operate at a comparatively high gain, and when impulse noise is present, it is readily detectable in the passbands of both bandpass filters at once. If the amplifier gain is high enough, however, comparator 22c or comparator 22d may also begin to detect system noise generated by, for example, a microprocessor controlling a display device or performing other control functions in the radio receiver. This type of system noise is weaker than the impulse noise generated by, for example, an automobile ignition system, and is more restricted in frequency. Thus if system noise is detected at all, it is generally detected by only one of the two comparators 22c and 22d.

**[0049]** By performing OR logic when the received signal level is high and performing AND logic when the received signal level is low, the output logic processor 26 is able to detect impulse noise mixed with a strong received FM broadcast signal, without detecting system noise at low received FM signal levels.

**[0050]** Next, a fourth embodiment will be described.

**[0051]** Referring to FIG. 13, the fourth embodiment comprises the same elements as in the first embodiment, but provides the second multiplier 21e with an indicator signal from the digital input terminal 27 described in the third embodiment, indicating the received signal level at the antenna. The multiplier 21e adjusts the value of the factor M in an inverse relation to the indicator signal, using a large value of M when the received signal level is low, and a small value of M when the received signal level is high.

**[0052]** As explained in the third embodiment, when the received signal level is high, front-end amplifier gain is reduced, thereby reducing the level of externally-generated impulse noise in relation to internally-generated background noise. By reducing the factor M in this situation, the fourth embodiment reduces the detection threshold, so that impulse noise can still be detected with adequate sensitivity.

**[0053]** When the received signal level is low, front-end amplifier gain is increased, creating a risk that the comparator 22 might detect not only impulse noise, but also system noise and other non-impulsive environmental noise. By increasing the factor M in this case, the fourth embodiment increases the detection threshold, so that only strong impulse noise such as ignition noise is detected.

**[0054]** As a result, the fourth embodiment is able to detect impulse noise accurately over a wider range of received FM signal levels than the first embodiment.

**[0055]** The indicator signal received at the digital input terminal 27 in the fourth embodiment need not be a one-bit signal. The indicator signal can be a multiple-bit signal indicating the received FM signal level in a substantially continuous manner, and the factor M can be varied in a substantially continuous manner in response to the received signal level.

**[0056]** Next, a second example will be described.

**[0057]** Referring to FIG. 14, the second example is a noise reduction system 28 comprising the impulse noise detector 29 of the first example and an impulse noise reducer 30. This noise reduction system 28 can be used in a radio receiver comprising an antenna 1, a tuner 2, an FM demodulator 3, an FM stereo demodulator 6, and a pair of loudspeakers 7, as shown, or in various other types of equipment in which impulse noise must be suppressed. In response to the impulse noise detection signal output by the impulse noise detector 29, the impulse noise reducer 30 attenuates or blocks the input received from, for example, the FM demodulator 3, and provides the attenuated signal or a substitute signal to, for example, the FM stereo demodulator 6. The impulse noise

detector 29 and noise reducer 30 may be combined into a single integrated circuit.

**[0058]** The second example provides the advantages noted in the first example. In an embodiment which is a variation of the second example the impulse noise detector of the first, second, third, or fourth embodiment is used.

**[0059]** As described above, by using a detection threshold obtained from the average of data values over an interval the first example detects short-duration impulse noise while ignoring other abruptly rising signals. By repeating the averaging process, the first embodiment obtains a detection threshold that is not affected by the noise impulses the detector is seeking to detect. By inserting a difference processor in the envelope detection process, the second embodiment detects impulse noise accurately despite the presence of leakage from the audio components of the analog audio signal. By detecting impulse noise separately in two separate frequency bands, the third embodiment detects impulse noise accurately at both high and low received radio-frequency signal levels, without detecting system noise. By varying the threshold multiplier M according to the received signal level, the fourth embodiment also detects impulse noise accurately at both high and low received radio-frequency signal levels without detecting system noise. The last example embodiment provides a noise reduction system with advantages similar to the above.

**[0060]** These features of the preceding embodiments can be combined in ways other than those illustrated in the drawings.

**[0061]** The input signal in which impulse noise is detected need not be an FM composite signal.

**[0062]** Those skilled in the art will recognize that further variations are possible within the scope claimed below.

## Claims

1. An impulse noise detector for detecting impulse noise in an input signal, the impulse noise detector having a first filter (9a) filtering said input signal to reject at least frequency components below a predetermined frequency, thereby producing a first filtered signal, a first envelope detector (16) coupled to said first filter (9a), detecting an envelope of said first filtered signal, thereby producing a first envelope signal, a first averager (20) coupled to said first envelope detector (16), calculating a first average value of at least some values of said first envelope signal over a predetermined time interval, a first multiplier (21) coupled to said first averager (20), multiplying said first average value by a first factor, thereby producing a threshold value, and a first comparator (22) coupled to said first multiplier (21), comparing at least one value of said first envelope

signal in said predetermined time interval with said threshold value, activating a first impulse noise detection signal if the compared value exceeds said threshold value, and de-activating said first detection signal if the compared value does not exceed said threshold value, **characterized in that** the impulse noise detector comprises:

a memory (17) coupled to said first envelope detector (16), storing said first envelope signal; a second averager (18) coupled to said memory (17), calculating a second average value of all of the values of said first envelope signal in said predetermined time interval; and a second multiplier (19) coupled to said second averager (18), multiplying said second average value by a second factor, thereby producing a product value; wherein said first averager (20) selects the values of said first envelope signal less than said product value for use in calculating said first average value.

2. The impulse noise detector of claim 1, wherein said first multiplier (21) increases said first factor when said first detection signal becomes inactive, and decreases said first factor when said first detection signal becomes active.

3. The impulse noise detector of claim 1, wherein said first envelope detector comprises:

a preliminary detector (16) detecting a preliminary envelope of said first filtered signal; and a difference processor (23) taking differences between values in said preliminary envelope, thereby producing a difference signal, and detecting an envelope of said difference signal, thereby producing said first envelope signal.

4. The impulse noise detector of claim 1, wherein said input signal is obtained from a radio-frequency signal received at an antenna (1), and said first multiplier (21e) adjusts said first factor according to a received level of said radio-frequency signal.

5. The impulse noise detector of claim 1, wherein said input signal is an FM composite signal.

6. The impulse noise detector of claim 1, wherein said first filter is a first bandpass filter (9c) having a first passband, the impulse noise detector further comprising:

a second filter (9d) having a second passband different from said first passband, filtering said input signal to produce a second filtered signal; a second envelope detector (16d) coupled to

said second filter (9d), detecting an envelope of said second filtered signal, thereby producing a second envelope signal; a third averager (20d) coupled to said second envelope detector (16d), calculating a third average value of at least some values of said second envelope signal over said predetermined time interval; a third multiplier (21d) coupled to said third averager (20d), multiplying said third average value by said first factor, thereby producing a second threshold value; a second comparator (22d) coupled to said third multiplier (21d), comparing at least one value of said second envelope signal in said predetermined time interval with said second threshold value, activating a second detection signal if the compared value exceeds said second threshold value, and de-activating said second detection signal if the compared value does not exceed said second threshold value; an input terminal (27) receiving an indicator signal that is active when said radio-frequency signal has a received level exceeding a predetermined level; and a logic circuit (26) coupled to said first comparator (22c), said second comparator (22d) and said input terminal (27), activating an impulse noise detection signal when at least two of said indicator signal, said first detection signal, and said second detection signal are active.

7. A noise reduction system comprising the impulse noise detector of claim 6 and a noise reducer (30) coupled to said logic circuit (26), the noise reducer passing said input signal when said impulse noise detection signal is inactive, and blocking said input signal when said impulse noise detection signal is active.

8. A noise reduction system comprising the impulse noise detector of claim 1 and a noise reducer (30) coupled to said first comparator (22), the noise reducer passing said input signal when said first detection signal is inactive, and blocking said input signal when said first detection signal is active.

9. The noise reduction system of claim 8, wherein said first multiplier (21) increases said first factor when said impulse noise detection signal becomes inactive, and decreases said first factor when said impulse noise detection signal becomes active.

10. The noise reduction system of claim 8, further comprising:

a memory (17) coupled to said envelope detector (16), storing said envelope signal;

a second averager (18) coupled to said memory (17), calculating a second average value of all of the values of said envelope signal in said predetermined time interval; and  
 a second multiplier (19) coupled to said second averager (18), multiplying said second average value by a second factor, thereby producing a product value; wherein  
 said first averager (20) selects the values of said envelope signal less than said product value for use in calculating said first average value.

11. The noise reduction system of claim 8, wherein said envelope detector comprises:

a preliminary detector (16) detecting a preliminary envelope of said filtered signal; and  
 a difference processor (23) taking differences between values in said preliminary envelope, thereby producing a difference signal, and detecting an envelope of said difference signal, thereby producing said envelope signal.

12. The noise reduction system of claim 8, wherein said input signal is obtained from a radio-frequency signal received at an antenna (1), and said first multiplier (21e) adjusts said first factor according to a received level of said radio-frequency signal.

13. The noise reduction system of claim 8, wherein said input signal is an FM composite signal.

#### Patentansprüche

1. Impulsrauschen-Detektor zum Erfassen von Impulsrauschen in einem Eingangssignal, welcher Impulsrauschen-Detektor aufweist: ein erstes Filter (9a), das das Eingangssignal filtert, um zumindest Frequenzkomponenten unterhalb einer vorbestimmten Frequenz zurückzuweisen, wodurch ein erstes gefiltertes Signal erzeugt wird, einen ersten Umhüllungsdetektor (16), der mit dem ersten Filter (9a) gekoppelt ist und eine Umhüllung des ersten gefilterten Signals erfasst, wodurch ein erstes Umhüllungssignal erzeugt wird, eine erste Durchschnittswert-Bildungsvorrichtung (20), die mit dem ersten Umhüllungsdetektor (16) gekoppelt ist und einen ersten Durchschnittswert von zumindest einigen Werten des ersten Umhüllungssignals über ein vorbestimmtes Zeitintervall berechnet, eine erste Multiplikationsvorrichtung (21), die mit der ersten Durchschnittswert-Bildungsvorrichtung (20) gekoppelt ist und den ersten Durchschnittswert mit einem ersten Faktor multipliziert, wodurch ein Schwellenwert erzeugt wird, und einen ersten Komparator (22), der mit der ersten Multiplikationsvorrichtung

(21) gekoppelt ist, zumindest einen Wert des ersten Umhüllungssignals in dem vorbestimmten Zeitintervall mit dem Schwellenwert vergleicht, ein erstes Impulsrauschen-Erfassungssignal aktiviert, wenn der verglichene Wert den Schwellenwert überschreitet, und das erste Erfassungssignal deaktiviert, wenn der verglichene Wert den Schwellenwert nicht überschreitet, **dadurch gekennzeichnet, dass** der Impulsrauschen-Detektor aufweist:

einen mit dem ersten Umhüllungsdetektor (16) gekoppelten Speicher (17), der das erste Umhüllungssignal speichert;

eine mit dem zweiten Speicher (17) gekoppelte zweite Durchschnittswert-Bildungsvorrichtung (18), die einen zweiten Durchschnittswert von allen Werten des ersten Umhüllungssignals in dem vorbestimmten Zeitintervall berechnet; und

eine mit der zweiten Durchschnittswert-Bildungsvorrichtung (18) gekoppelte zweite Multiplikationsvorrichtung (19), die den zweiten Durchschnittswert mit einem zweiten Faktor multipliziert, wodurch ein Produktwert erzeugt wird; worin

die erste Durchschnittswert-Bildungsvorrichtung (20) die Werte des ersten Umhüllungssignals, die niedriger als der Produktwert sind, für die Verwendung bei der Berechnung des ersten Durchschnittswerts auswählt.

2. Impulsrauschen-Detektor nach Anspruch 1, bei dem die erste Multiplikationsvorrichtung (21) den ersten Faktor erhöht, wenn das erste Erfassungssignal inaktiv wird, und den ersten Faktor erniedrigt, wenn das erste Erfassungssignal aktiv wird.

3. Impulsrauschen-Detektor nach Anspruch 1, bei dem der erste Umhüllungsdetektor aufweist:

einen vorläufigen Detektor (16), der eine vorläufige Umhüllung des ersten gefilterten Signals erfasst; und

einen Differenzprozessor (23), der Differenzen zwischen Werten in der vorläufigen Umhüllung aufnimmt, wodurch ein Differenzsignal erzeugt wird, und eine Umhüllung des Differenzsignals erfasst, wodurch das erste Umhüllungssignal erzeugt wird.

4. Impulsrauschen-Detektor nach Anspruch 1, bei dem das Eingangssignal aus einem an einer Antenne (1) empfangenen Hochfrequenzsignal erhalten ist, und die erste Multiplikationsvorrichtung (21e) den ersten Faktor gemäß einem empfangenen Pegel des Hochfrequenzsignals einstellt.

5. Impulsrauschen-Detektor nach Anspruch 1, bei



dem das Eingangssignal ein FM-Signalgemisch ist.

6. Impulsrauschen-Detektor nach Anspruch 1, bei dem das erste Filter ein erstes Bandpassfilter (9c) mit einem ersten Durchlassband ist, wobei der Impulsrauschen-Detektor weiterhin aufweist:
- ein zweites Filter (9d) mit einem zweiten Durchlassband, dass gegenüber dem ersten Durchlassband unterschiedlich ist, welches das Eingangssignal filtert, um ein zweites gefiltertes Signal zu erzeugen;
  - einen zweiten Umhüllungsdetektor (16d), der mit dem zweiten Filter (9d) gekoppelt ist und eine Umhüllung des zweiten gefilterten Signals erfasst, wodurch ein zweites Umhüllungssignal erzeugt wird;
  - eine dritte Durchschnittswert-Bildungsvorrichtung (20d), die mit dem zweiten Umhüllungsdetektor (16d) gekoppelt ist und einen dritten Durchschnittswert von zumindest einigen Werten des zweiten Umhüllungssignals über ein vorbestimmtes Zeitintervall berechnet;
  - eine dritte Multiplikationsvorrichtung (21d), die mit der dritten Durchschnittswert-Bildungsvorrichtung (20d) gekoppelt ist und den dritten Durchschnittswert mit dem ersten Faktor multipliziert, wodurch ein zweiter Schwellenwert erzeugt wird;
  - einen zweiten Komparator (22d), der mit der dritten Multiplikationsvorrichtung (21d) gekoppelt ist, zumindest einen Wert des zweiten Umhüllungssignals in dem vorbestimmten Zeitintervall mit dem zweiten Schwellenwert vergleicht, ein zweites Erfassungssignal aktiviert, wenn der verglichene Wert den zweiten Schwellenwert überschreitet, und das zweite Erfassungssignal deaktiviert, wenn der verglichene Wert den zweiten Schwellenwert nicht überschreitet;
  - einen Eingangsanschluss (27), der ein Indikatorsignal empfängt, das aktiv ist, wenn das Hochfrequenzsignal einen empfangenen Pegel hat, der einen vorbestimmten Pegel überschreitet;
  - eine logische Schaltung (26), die mit dem ersten Komparator (22c), dem zweiten Komparator (22d) und dem Eingangsanschluss (27) gekoppelt ist und
  - ein Impulsrauschen-Erfassungssignal aktiviert, wenn zumindest zwei von dem Indikatorsignal, dem ersten Erfassungssignal und dem zweiten Erfassungssignal aktiv sind.
7. Rauschreduktionssystem, aufweisend den Impulsrauschen-Detektor nach Anspruch 6 und eine Rauschreduktionsvorrichtung (30), die mit der logischen Schaltung (26) gekoppelt ist, wobei die

Rauschreduktionsvorrichtung das Eingangssignal durchlässt, wenn das Impulsrauschen-Erfassungssignal inaktiv ist, und das Eingangssignal sperrt, wenn das Impulsrauschen-Erfassungssignal aktiv ist.

8. Rauschreduktionssystem, aufweisend den Impulsrauschen-Detektor nach Anspruch 1 und eine Rauschreduktionsvorrichtung (30), die mit dem ersten Komparator (22) gekoppelt ist, wobei die Rauschreduktionsvorrichtung das Eingangssignal durchlässt, wenn das erste Erfassungssignal inaktiv ist, und das Eingangssignal sperrt, wenn das erste Erfassungssignal aktiv ist.
9. Rauschreduktionssystem nach Anspruch 8, bei dem die erste Multiplikationsvorrichtung (21) den ersten Faktor erhöht, wenn das Impulsrauschen-Erfassungssignal inaktiv wird, und den ersten Faktor erniedrigt, wenn das Impulsrauschen-Erfassungssignal aktiv wird.
10. Rauschreduktionssystem nach Anspruch 8, weiterhin aufweisend:
- einen mit dem Umhüllungsdetektor (16) gekoppelten Speicher (17), der das Umhüllungssignal speichert;
  - eine mit dem Speicher (17) gekoppelte zweite Durchschnittswert-Bildungsvorrichtung (18), die einen zweiten Durchschnittswert aus allen Werten des Umhüllungssignals in dem vorbestimmten Zeitintervall berechnet; und
  - eine mit der zweiten Durchschnittswert-Bildungsvorrichtung (18) gekoppelte zweite Multiplikationsvorrichtung (19), die den zweiten Durchschnittswert mit einem zweiten Faktor multipliziert, wodurch ein Produktwert erzeugt wird; worin
  - die erste Durchschnittswert-Bildungsvorrichtung (20) die Werte des Umhüllungssignals, die niedriger als der Produktwert sind, für die Verwendung bei der Berechnung des ersten Durchschnittswerts auswählt.
11. Rauschreduktionssystem nach Anspruch 8, bei der der Umhüllungsdetektor aufweist:

- einen vorläufigen Detektor (16), der eine vorläufige Umhüllung des gefilterten Signals erfasst; und
- einen Differenzprozessor (23), der Differenzen zwischen Werten in der vorläufigen Umhüllung aufnimmt, wodurch er ein Differenzsignal erzeugt, und eine Umhüllung des Differenzsignals erfasst, wodurch er das Umhüllungssignal erzeugt.

12. Rauschreduktionssystem nach Anspruch 8, bei dem das Eingangssignal aus einem an einer Antenne (1) empfangenen Hochfrequenzsignal erhalten ist, und die erste Multiplikationsvorrichtung (21e) den ersten Faktor gemäß einem empfangenen Pegel des Hochfrequenzsignals einstellt. 5
13. Rauschreduktionssystem nach Anspruch 8, bei dem das Eingangssignal ein FM-Mischsignal ist. 10

## Revendications

1. Détecteur de bruit impulsionnel pour détecter un bruit impulsionnel dans un signal d'entrée, le détecteur de bruit impulsionnel comprenant un premier filtre (9a) filtrant ledit signal d'entrée pour rejeter au moins les composantes de fréquences inférieures à une fréquence prédéterminée, de manière à produire un premier signal filtré, un premier détecteur d'enveloppe (16) couplé audit premier filtre (9a), détectant une enveloppe dudit premier signal filtré, en produisant ainsi un premier signal d'enveloppe, un premier dispositif (20) de formation de la moyenne couplé au premier détecteur d'enveloppe (16), calculant une première valeur moyenne d'au moins certaines valeurs dudit premier signal d'enveloppe pendant un intervalle de temps prédéterminé, un premier multiplieur (21) couplé audit premier dispositif (20) de formation de la moyenne, multipliant ladite première valeur moyenne par un premier facteur, en produisant ainsi une valeur de seuil, et un premier comparateur (22) couplé audit premier multiplieur (21) comparant au moins une valeur dudit premier signal d'enveloppe dans ledit intervalle de temps prédéterminé, à ladite valeur de seuil, activant un premier signal de détection de bruit impulsionnel si la valeur comparée dépasse ladite valeur de seuil, et désactive ledit premier signal de détection si la valeur comparée n'excède pas ladite valeur de seuil, **caractérisé en ce que** le détecteur de bruit impulsionnel comprend: 40

une mémoire (17) couplée audit premier détecteur d'enveloppe (16) et qui mémorise ledit premier signal d'enveloppe; 45

un second dispositif (18) de formation de la moyenne couplée à ladite mémoire (17) et qui calcule une seconde valeur moyenne de l'ensemble des valeurs dudit premier signal d'enveloppe dans ledit intervalle de temps prédéterminé; et 50

un second multiplieur (19) couplé audit second dispositif (18) de formation de la moyenne et multipliant ladite seconde valeur moyenne par un second facteur, en produisant ainsi une valeur de produit; dans lequel 55

ledit premier dispositif (20) de formation de la

valeur moyenne sélectionne les valeurs dudit premier signal d'enveloppe inférieures à ladite valeur de produit, pour leur utilisation dans le calcul de ladite première valeur moyenne.

2. Détecteur de bruit impulsionnel selon la revendication 1, dans lequel ledit premier multiplieur (21) augmente ledit premier facteur lorsque ledit premier signal de détection devient inactif, et réduit ledit premier facteur lorsque ledit premier signal de détection devient actif.

3. Détecteur de bruit impulsionnel selon la revendication 1, dans lequel ledit premier détecteur d'enveloppe comprend:

un détecteur préliminaire (16) détectant une enveloppe préliminaire dudit premier signal filtré; et

un processeur de différence (23) qui forme des différences entre des valeurs dans ladite enveloppe préliminaire, en produisant ainsi un signal de différence, et qui détecte une enveloppe dudit signal de référence, en produisant ainsi ledit premier signal d'enveloppe.

4. Détecteur de bruit impulsionnel selon la revendication 1, dans lequel ledit signal d'entrée est obtenu à partir d'un signal à fréquence radio reçu au niveau d'une antenne (1) et ledit premier multiplieur (21e) ajuste ledit premier facteur conformément à un niveau reçu dudit signal à fréquence radio.

5. Détecteur de bruit impulsionnel selon la revendication 1, dans lequel ledit signal d'entrée est un signal composite FM.

6. Détecteur de bruit impulsionnel (1) selon la revendication 1, dans lequel ledit premier filtre est un premier filtre passe-bande (9a) comportant une première bande passante, le détecteur de bruit impulsionnel comprenant en outre:

un second filtre (9d) possédant une seconde bande passante différente de ladite première bande passante, et qui filtre ledit signal d'entrée pour produire un second signal filtré;

un second détecteur d'enveloppe (16d) couplé audit second filtre (9d), qui détecte une enveloppe dudit second signal filtré, en produisant ainsi un second signal d'enveloppe;

un troisième dispositif (20d) de formation de la moyenne couplé audit second détecteur d'enveloppe (16d) et qui calcule une troisième valeur moyenne d'au moins certaines valeurs dudit second signal d'enveloppe pendant ledit intervalle de temps prédéterminé;

un troisième multiplieur (21d) couplé audit troi-

sième dispositif (20d) de formation de la moyenne et qui multiplie ladite troisième valeur moyenne par ledit premier facteur, en produisant ainsi une seconde valeur de seuil;

un second comparateur (22d) couplé audit troisième multiplieur (21d), comparant au moins une valeur dudit second signal d'enveloppe dans ledit intervalle de temps prédéterminé à ladite seconde valeur de seuil, activant un second signal de détection si la valeur comparée dépasse ladite seconde valeur de seuil, et désactivant ledit second signal de détection si la valeur comparée ne dépasse pas ladite seconde valeur de seuil;

une borne d'entrée (27) recevant un signal indicateur qui est actif lorsque ledit signal à fréquence radio possède un niveau reçu qui dépasse un seuil prédéterminé; et

un circuit logique (26) couplé audit premier comparateur (22c), ledit second comparateur (22d) et ladite borne d'entrée (27) activant un signal de détection de bruit impulsionnel lorsqu'au moins deux dudit signal indicateur, dudit premier signal de détection et dudit second signal de détection sont actifs.

7. Système de réduction de bruit comprenant le détecteur de bruit impulsionnel selon la revendication 6 et un réducteur de bruit (30) couplé audit circuit logique (26), le réducteur de bruit transmettant ledit signal d'entrée lorsque ledit signal de détection de bruit impulsionnel est inactif et bloquant ledit signal d'entrée lorsque ledit signal de détection de bruit impulsionnel est actif.

8. Système de réduction de bruit comprenant le détecteur de bruit impulsionnel selon la revendication 1 et un réducteur de bruit (30) couplé audit premier comparateur (22), le réducteur de bruit transmettant ledit signal d'entrée lorsque ledit premier signal de détection est inactif, et bloquant ledit signal d'entrée lorsque ledit premier signal de détection est actif.

9. Système de réduction de bruit selon la revendication 8, dans lequel ledit premier multiplieur (21) augmente ledit premier facteur lorsque ledit signal de détection de bruit impulsionnel devient inactif, et diminue ledit premier facteur lorsque ledit signal de détection de bruit impulsionnel devient actif.

10. Système de réduction de bruit selon la revendication 8, comprenant en outre:

une mémoire (17) couplée audit détecteur d'enveloppe (16), mémorisant ledit signal d'enveloppe;

un second dispositif (18) de formation de la

moyenne couplé à ladite mémoire (17), qui calcule une seconde valeur moyenne de l'ensemble des valeurs dudit signal d'enveloppe pendant ledit intervalle de temps prédéterminé; et un second multiplieur (19) couplé audit second dispositif (18) de formation de la moyenne et qui multiplie ladite seconde valeur moyenne par un second facteur, en produisant ainsi une valeur de produit;

dans lequel ledit premier dispositif (20) de formation de la moyenne sélectionne les valeurs dudit signal d'enveloppe inférieures à ladite valeur de produit pour leur utilisation dans le calcul de ladite première valeur moyenne.

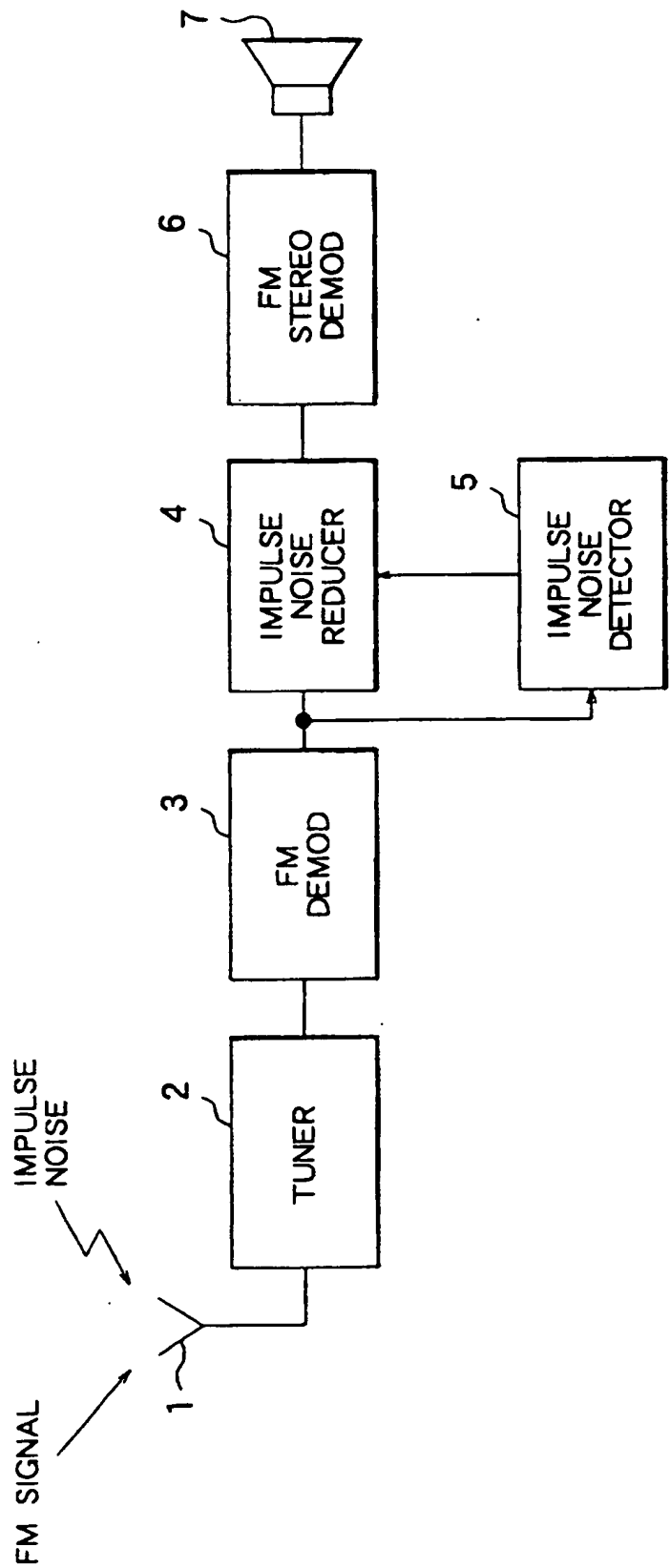
11. Système de réduction de bruit selon la revendication 8, dans lequel le détecteur d'enveloppe comprend:

un détecteur préliminaire (16) détectant une enveloppe préliminaire dudit signal filtré; et un processeur de différence (23) qui forme des différences entre les valeurs dans ladite enveloppe préliminaire, en produisant ainsi un signal de différence, et qui détecte une enveloppe dudit signal de différence, en produisant ainsi ledit signal d'enveloppe.

12. Système de réduction de bruit selon la revendication 8, dans lequel ledit signal d'entrée est obtenu à partir d'un signal à fréquence radio reçu au niveau d'une antenne (1), et ledit premier multiplieur (21a) ajuste ledit premier facteur conformément à un niveau reçu dudit signal à fréquence radio.

13. Système de réduction de bruit selon la revendication 8, dans lequel ledit signal d'entrée est un signal composite FM.

FIG. 1  
PRIOR ART



**FIG. 2**  
PRIOR ART

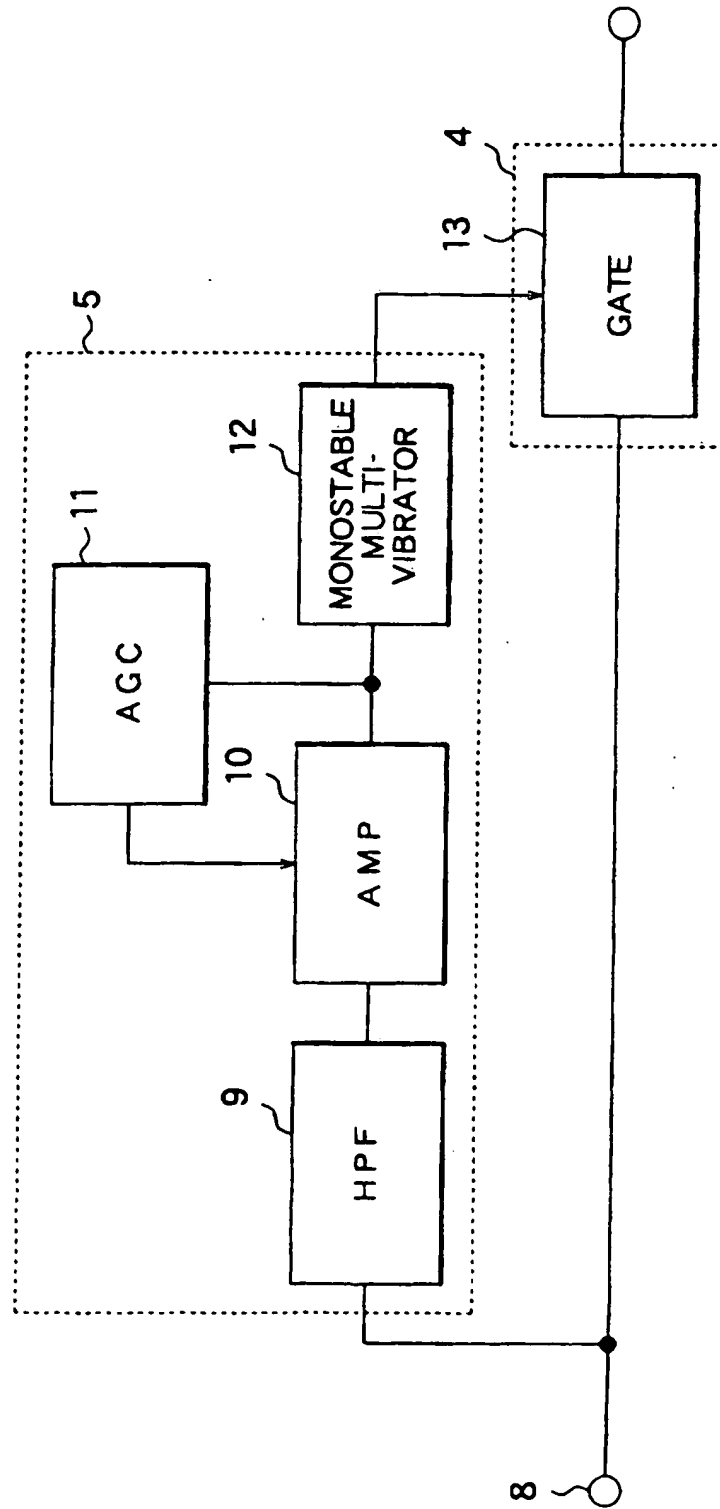


FIG. 3

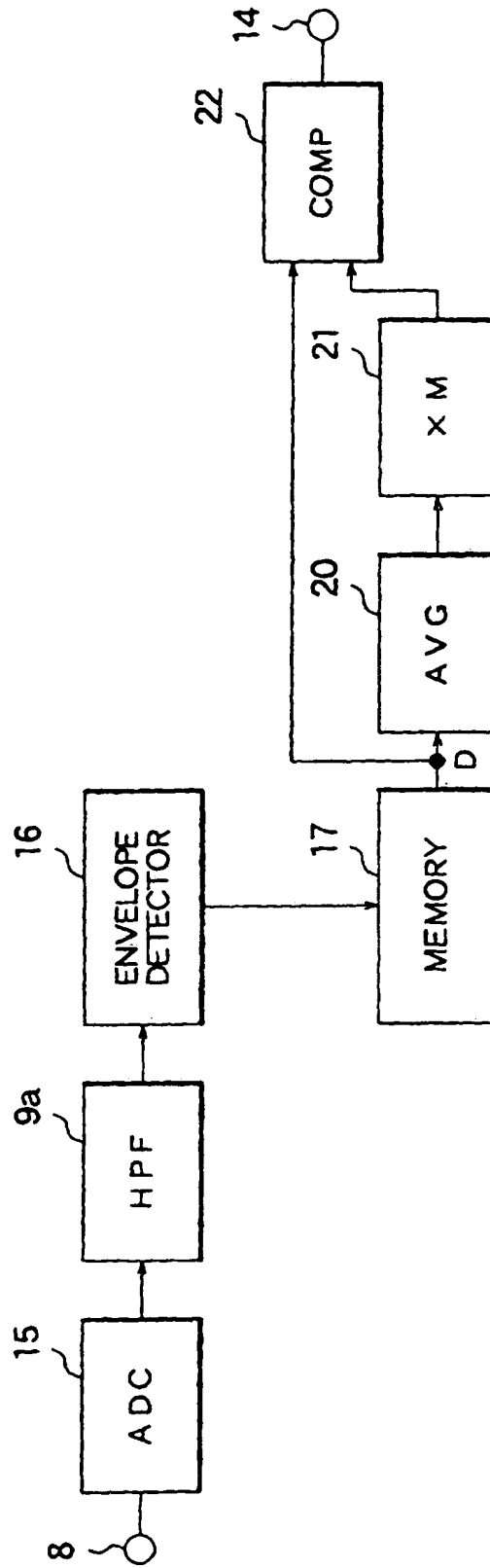


FIG. 4

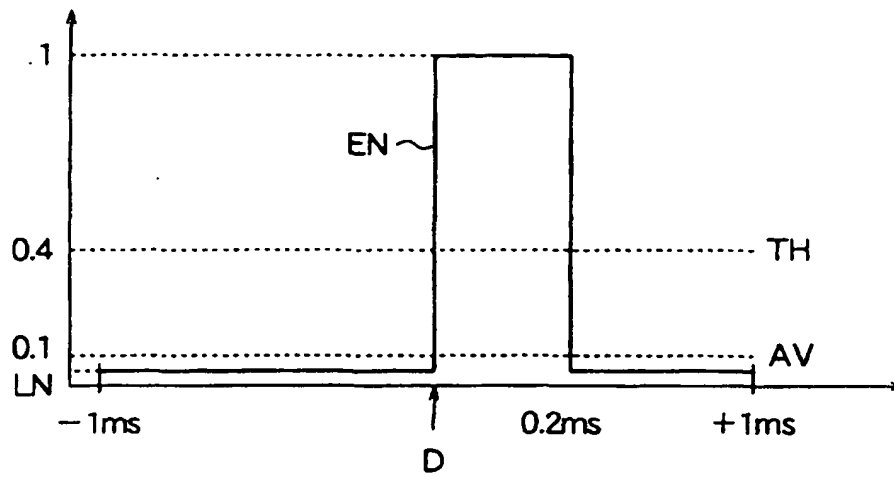


FIG. 5

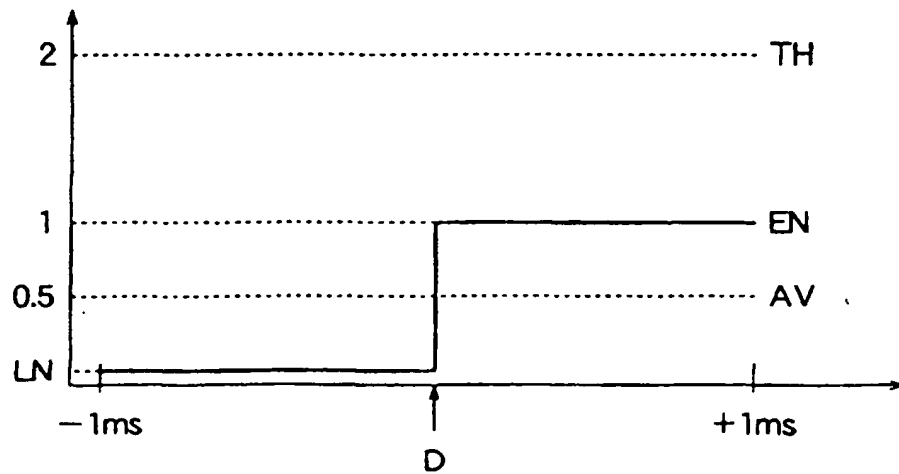


FIG. 6

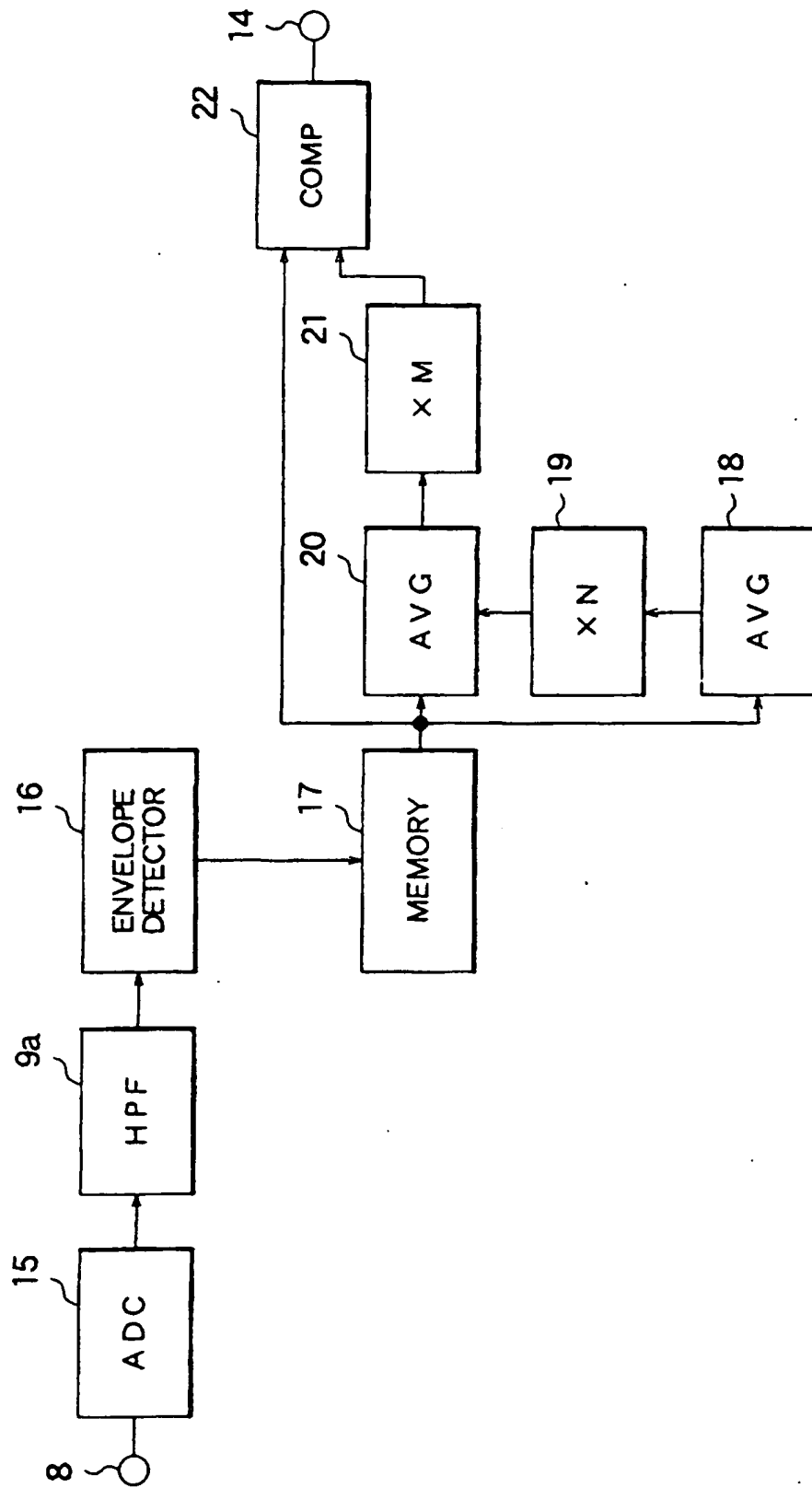




FIG. 7

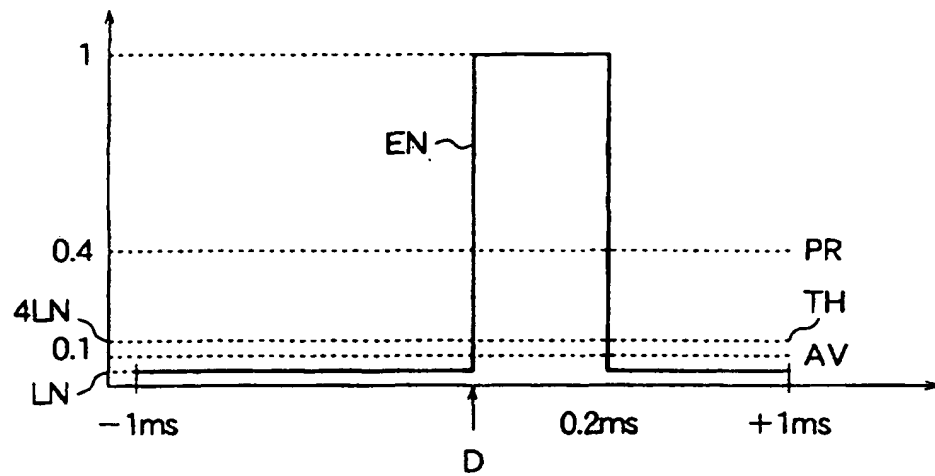


FIG. 8

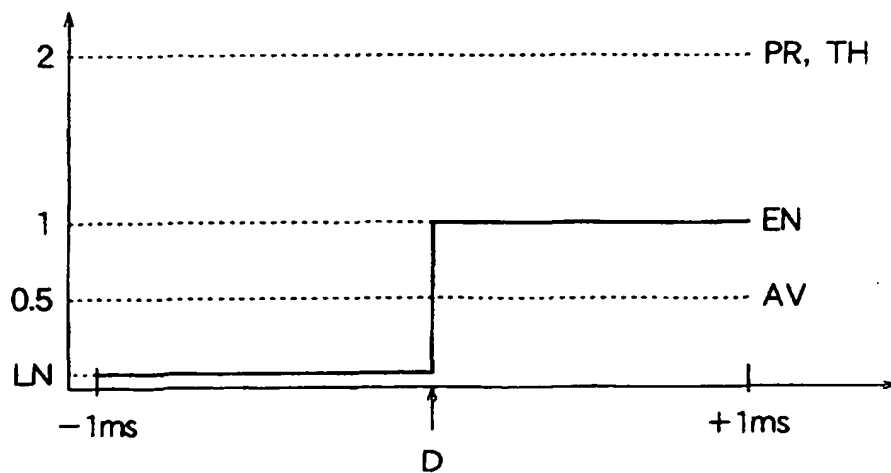


FIG. 9

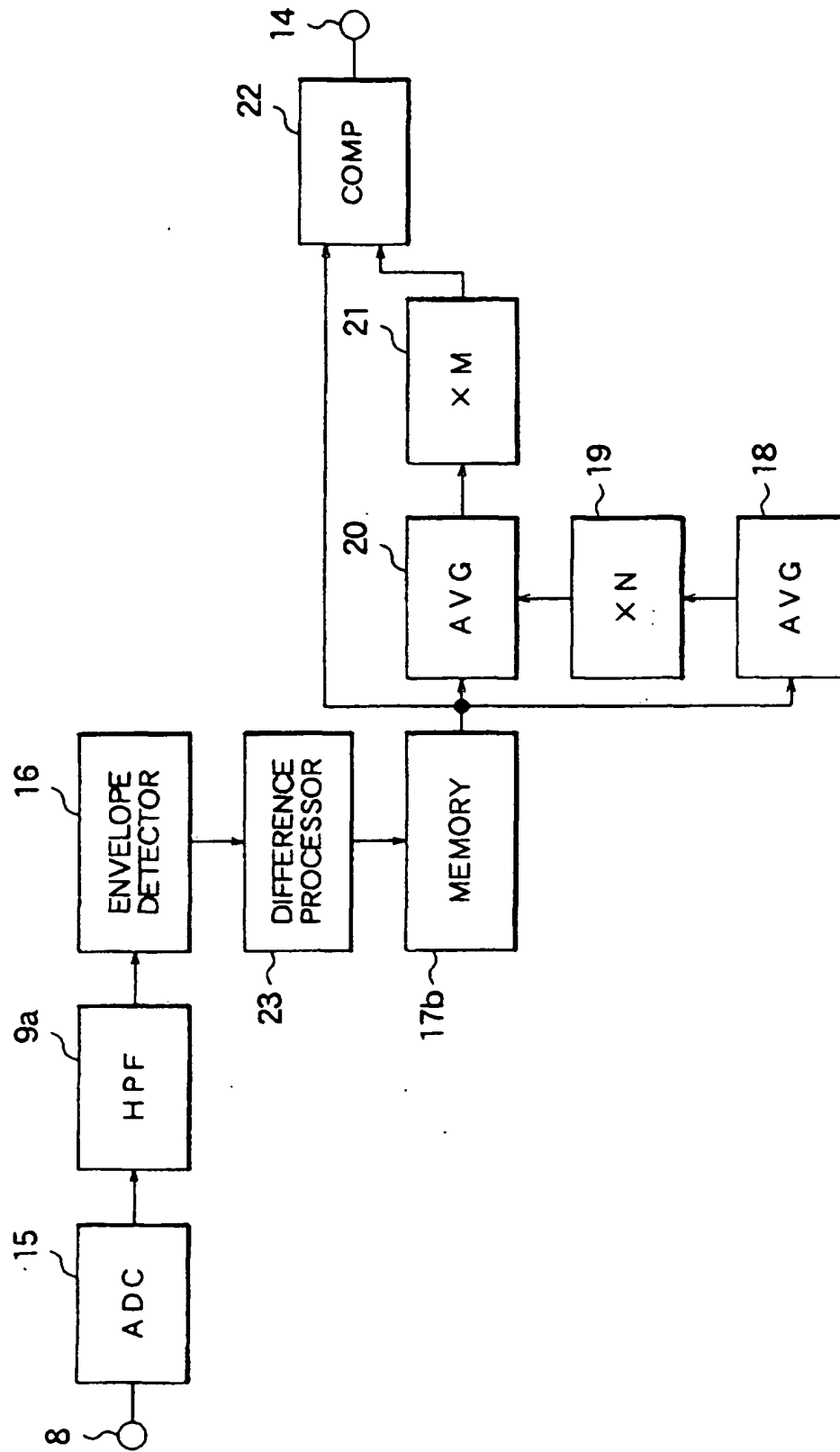


FIG. 10

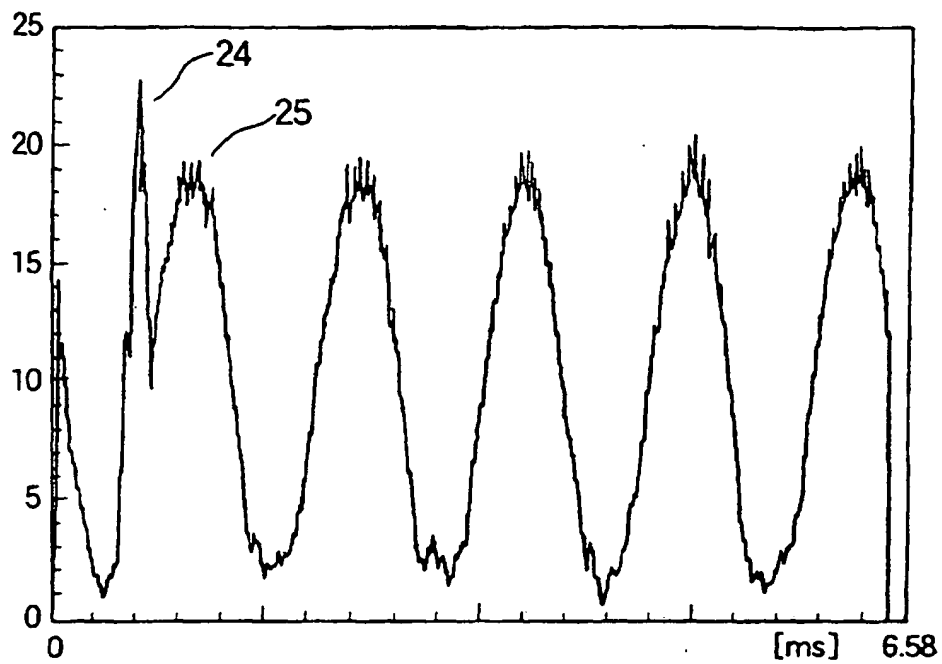


FIG. 11

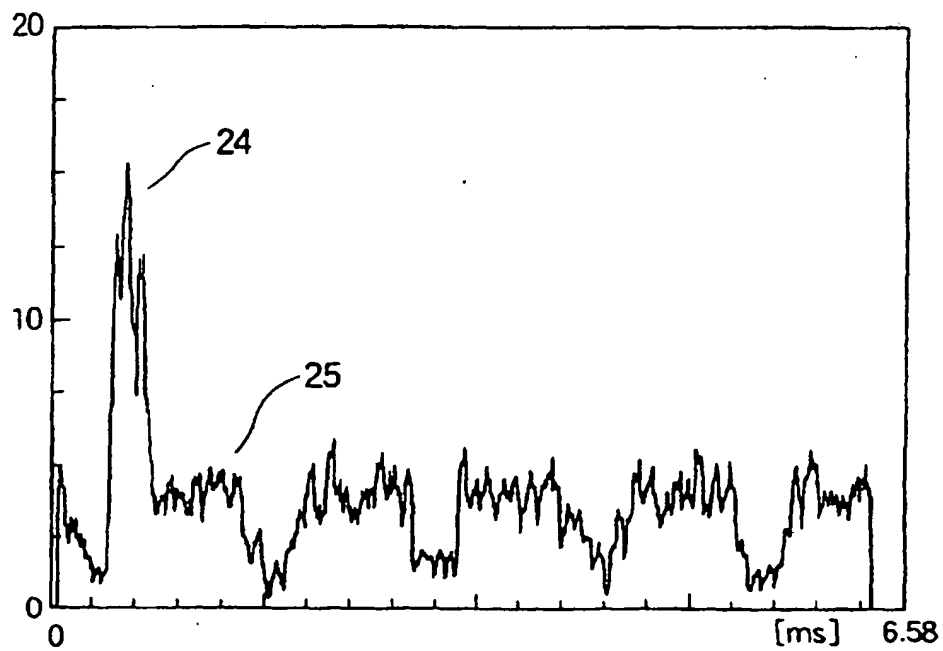


FIG. 12

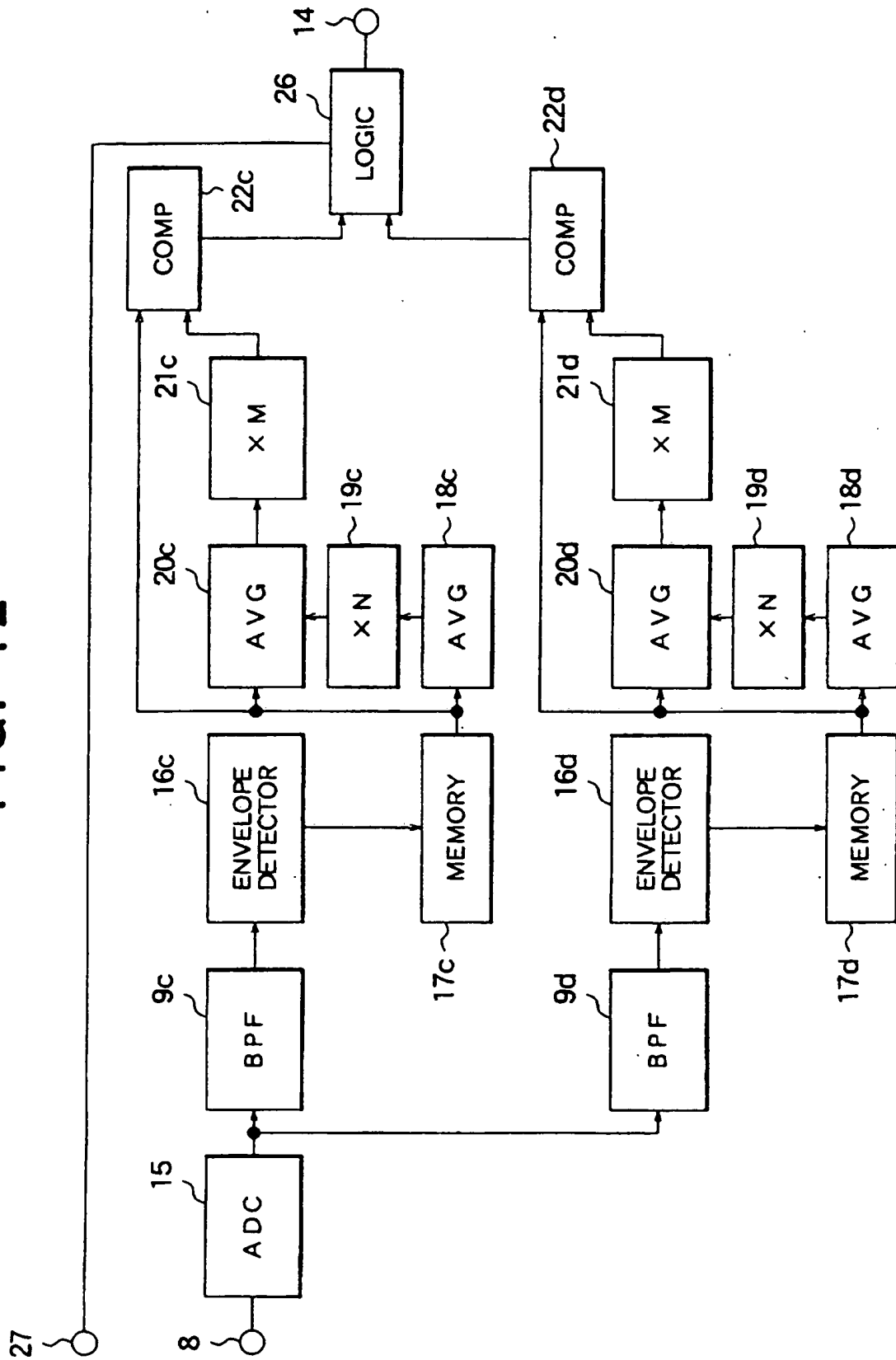


FIG. 13

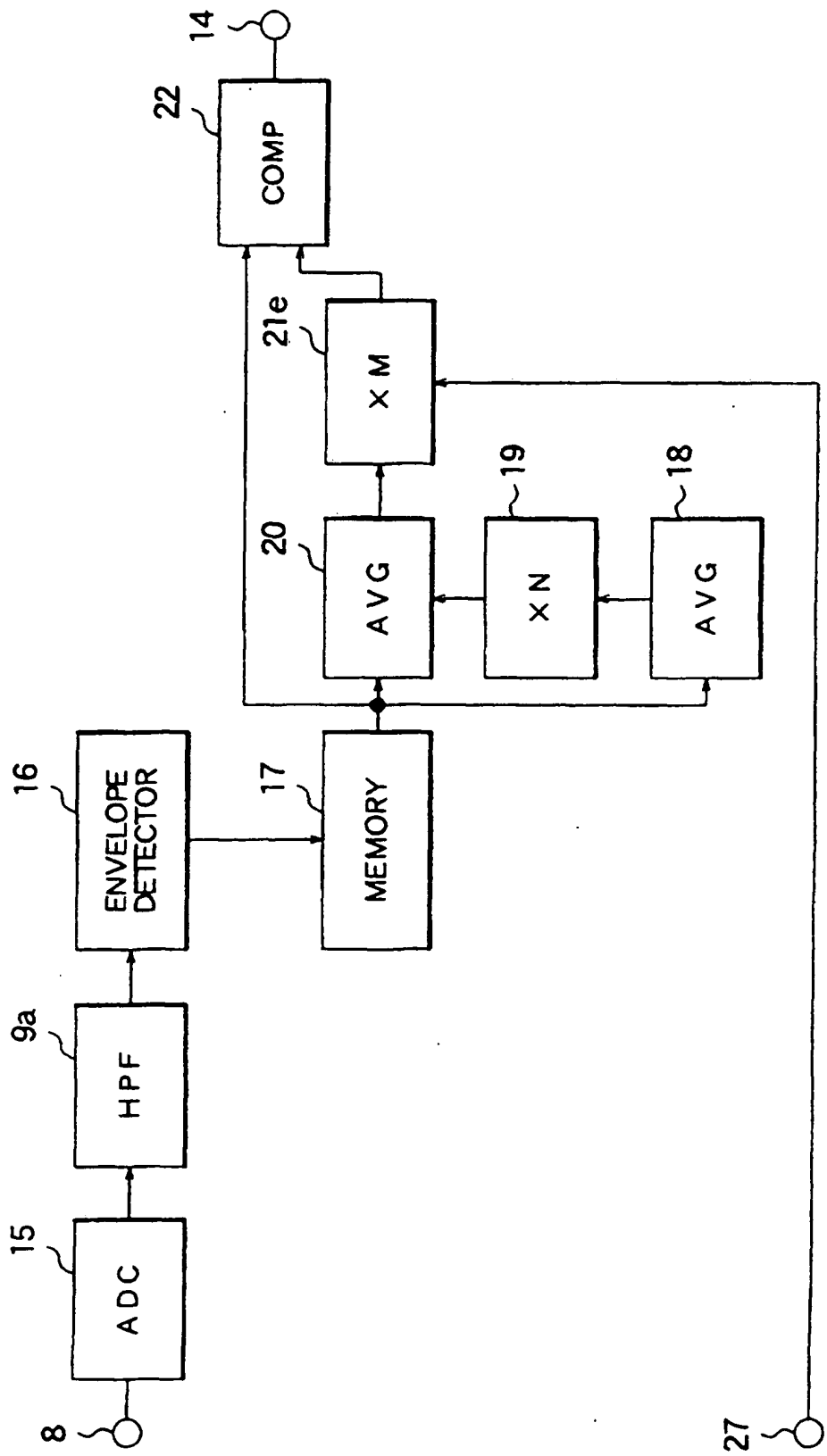


FIG. 14

